HDice-UITF Fast Shutdown (last update: Aug 25/2020)

source	Mnemonic	FSD inputs	UITF FSD inputs	FSD condition	source	responsible person	All OK condition
844 ch 1	**IBCvac, DUMPvac**	**FSD 1**	IBC vac gauge	bad vacuum - common	RC rack	Tom/Kevin	+5 V
		FSD 1	Dump vacuum gauge	bad vacuum - common	RC rack	Tom/Kevin	+5 V
844 ch 2	**BFIELDtrans, BFIELDmain**	**FSD 2**	IBC Transfer solenoid	IBC Solenoid(s) fields low	Bell Gauss Meters => RC rack	Kevin	+5 V
		FSD 2	IBC Main solenoid	IBC Solenoid(s) fields low	Bell Gauss Meters => RC rack	Kevin	+5 V
844 ch3	**TransCOIL, MainCOIL**	**FSD 3**	IBC OMEGAs trigger Transfer rampdown	IBC solenoid(s) ramp down triggered	Oxfors IPS(s) => RC rack	Mike Lowry, Kevin	+5 V
		FSD 3	IBC OMEGAs trigger Main rampdown	IBC solenoid(s) ramp down triggered	Oxfors IPS(s) => RC rack	Mike Lowry, Kevin	+5 V
844 ch4	**BFIELDdump**	**FSD 4**	Dump solenoid	Dump solenoid field low	Bell Gauss Meter => RC rack	Kevin	+5 V
844 ch5	**RC#1**	**FSD 5**	Rogowski Coil #1	beam off-center	RC rack	Kevin Wei	+5 V
IBCraspB ch1	**A1rad**	**FSD 6**	1 Ferrara counter at upstream aperture 1	high rate <=> beam off-center	Halo FPGA	Luca Barion; Tom	+5 V
IBCraspB ch5	**A2rad**	**FSD 7**	2 Ferrara counters at upstream aperture 2	high rate <=> beam off-center	Halo FPGA	Luca Barion; Tom	+5 V
FPGA	**HALOasy**	**FSD 8**	Target Halo counters	ASY <=> beam off-center	Halo FPGA	Luca Barion; Tom	+5 V
IBCraspB ch2	**IDUMPrad**	**FSD 9**	Ferrara counter at beam dump	beam current too high	Halo FPGA	Luca Barion; Tom	+5 V
	Raster	FSD 10	Raster ON	beam stops moving	Raster electronics	Bill Gunning	