

# Mott Cabling

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# GATED SCALER S1

Control Channel	Signal
1 IN	LNE (Delayed nT_Settle)
2 IN	Delayed Helicity
3 IN	Pattern Sync
4 IN	GATE (nT_Settle)
5 OUT	
6 OUT	
7 OUT	
8 OUT	

S1 Chan	Signal
1	BCM0L02 (VtoF 0-10V)
2	Mott DetTr
3	L1A
4	121 kHz Clock
5	LEFT Coincidence
6	RIGHT Coincidence
7	UP Coincidence
8	DOWN Coincidence
9	E LEFT
10	E RIGHT
11	E UP
12	E DOWN
13	$\Delta E$ LEFT
14	$\Delta E$ RIGHT
15	$\Delta E$ UP
16	$\Delta E$ DOWN

S1 Chan	Signal	
17	BPM 0L01 X+	BPM 0L01
18	BPM 0L01 X-	
19	BPM 0L01 Y+	
20	BPM 0L01 Y-	
21	BPM 0L02 X+	BPM 0L02
22	BPM 0L02 X-	
23	BPM 0L02 Y+	
24	BPM 0L02 Y-	
25	BPM 5D00 X+	BPM 5D00
26	BPM 5D00 X-	
27	BPM 5D00 Y+	
28	BPM 5D00 Y-	
29	BPM 5D01 X+	BPM 5D01
30	BPM 5D01 X-	
31	BPM 5D01 Y+	
32	BPM 5D01 Y-	

# SCALER S2

Control Channel	Signal
1 IN	LNE (L1A)
2 – 4 IN	Empty
5 – 8 OUT	Empty

S2 Chan	Signal
1	
2	
3	
4	
5	
6	
7	
8	
9	
10	
11	
12	
13	
14	
15	
16	

S2 Chan	Signal
17	
18	
19	
20	
21	
22	
23	
24	
25	
26	
27	
28	
29	Delayed Helicity
30	T_Settle
31	nPattern-Sync
32	Pair-sync

# FADC

## Mott

FADC Chan	Signal
1	E LEFT
2	E RIGHT
3	E UP
4	E DOWN
5	$\Delta E$ LEFT
6	$\Delta E$ RIGHT
7	$\Delta E$ UP
8	$\Delta E$ DOWN
9	BFM
10	
11	Mott DetTr
12	
13	Delayed Helicity
14	T_Settle
15	Pattern-Sync
16	Pair-Sync

## FADC Int

FADC Chan	Signal
1	
2	
3	
4	
5	
6	
7	
8	
9	
10	Battery
11	
12	
13	Delayed Helicity
14	T_Settle
15	Pattern-Sync
16	Pair-Sync

# CHANNEL ASSIGNMENT

## CAEN V775 TDC

TDC Channel	Signal
1	
2	
3	
4	
5	
6	
7	
8	
9	
10	
11	
12	
13	
14	
15	
16	

TDC Channel	Signal
17	BFM
18	Mott DetTr
19 – 32	Empty