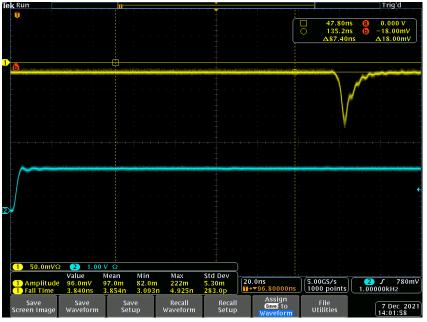
NPS PMT HV Divider – Status – 16 December 2021

F.J. Barbosa, V. Berdnikov, C. Stanislav

HV = 605 V @ 405 uA, Amplitude ~ 10% of Maximum





- Ver 2 PCB 4 layer + regulators
- Un-shielded
 - Noise: < 110 mVp-p</p>

- Ver 2 PCB 4 layer + regulators
- Shielded box
 - Noise: < 5 mVp-p -- very low!</p>



☐ Test to assess detector shielding impact on baseline noise.

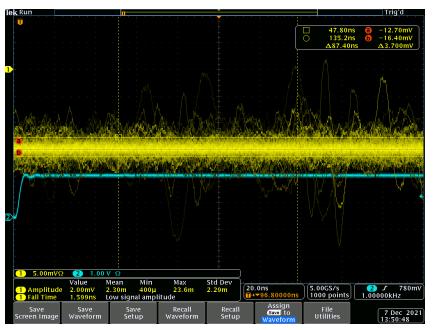


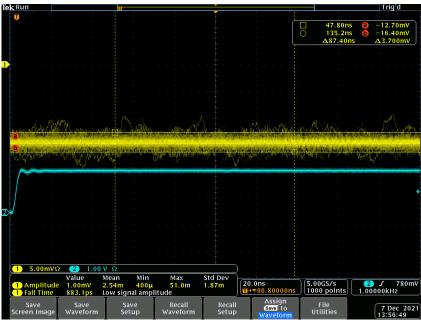






- Box is grounded from PCB through HV and signal connectors.
 - **□** NPS detector HV is floating and should be better.
- Tested with and without extra GND cable to clean GND.

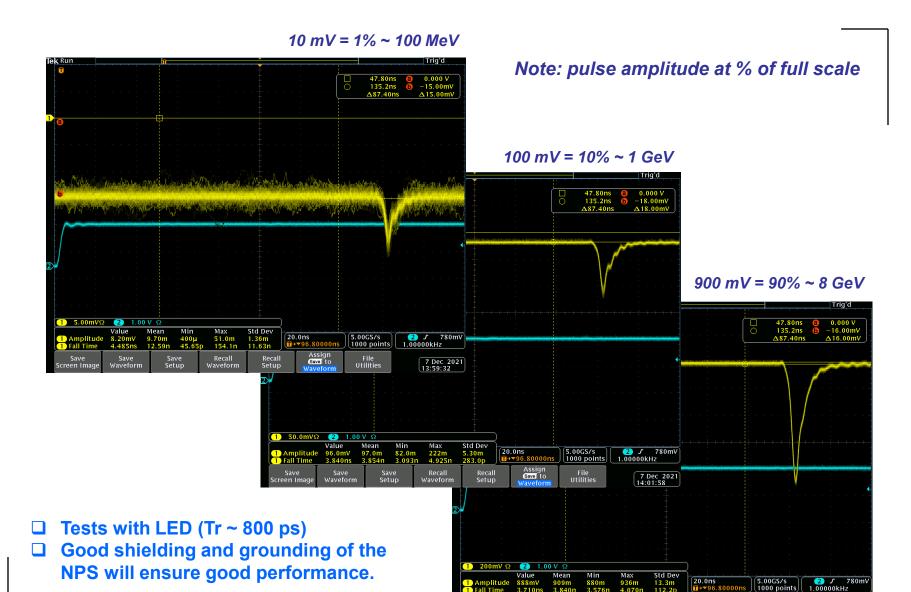




- Ver 2 PCB 4 layer + regulators
- · Shielded box via connectors
- Noise can be reduced by decreasing cable loop area

- Ver 2 PCB 4 layer + regulators
- Shielded box with GND cable to clean GND
- Noise is < 5 mVp-p</p>





Assign Save to

- Hybrid approach to manufacturing NPS dividers:
 - *Turn-key (4 5 weeks) but ...*
 - We procured components with long lead times and those in short supply: 2 connectors, transistors (4 weeks)
 - Total PCB assembly ~ 6 weeks
- Remove (from old PCBs) and install sockets: + 6 weeks
- Test: + 3 weeks
 - Total NPS Divider: 15 Weeks (late March early April 2022)
- Procurements have been completed:
 - □ Ordered components (long lead/short supply).
 - ☐ Ordered LV cables.
 - □ PO issued for PCB assembly contract.

