

Update from the FEG – Chris Cuevas

- ❑ **Mode 10 firmware modifications to the flash ADC-250** have been completed by Hai Dong. The firmware has been functionally verified and Hai will work with Ed Jastrzembski to run software verification in the lab. Careful verification will need to be performed to demonstrate that new firmware produces waveform data when pulse detection algorithm fails.
 - Presently both Hai and Ed are not on-site but have approval to work in the lab.
 - Estimate for test verification activity has not been evaluated, but Ed has similar software that can be modified for this test.

- ❑ **NPS LED pulser project** - This project has been assigned to William Gu and he has evaluated the requirements document from Carlos/Brad
 - Development of design specifications has started and the group has another LED pulser design [VME] that was used for the CLAS12 CTOF and HTCC
 - Preference to use VME as the interface/control method, but LED drive circuits may be closer to calorimeter block
 - No show stoppers, and will create schedule to show design, fabrication, procurement, assembly and test activities

- ❑ **NPS PMT – High Voltage Dividers** -