

LERF RF User Guide

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Contents

I.	Standard Controls and Information	2
1.	CPUs and IOCs	2
2.	Chassis IPs.....	2
3.	Start/Restart the EPICS IOC.....	2
4.	View RF EDM displays	4
5.	Initialize/Checkout LLRF Hardware	5
6.	View RF Waveform Plots.....	8
7.	Control Stepper Motor Tuner.....	10
8.	Control Piezo Tuner	11
9.	Drive Cavity With Simple Tone Signal	12
10.	Run RF In SEL Pulsed Mode	13
11.	Ramp Cavity to CW/SELAP and Perform Cavity Characterization.....	14
II.	Occasional Issues	15
1.	Mystery Rack Checkout Error	15
2.	Recover Non-Updating EPICS Waveforms	16
3.	Ping Test	17
4.	View the EPICS IOC Console	17
5.	RF 'Background Process'	18

I. Standard Controls and Information

1. CPUs and IOCs

JLab Cryomodule Number	LCLS-II Cryomodule Name	CPU Node Name*	EPICS IOC Name*
1	ACCL:L1B:0200	lcls-llrfcpu01	sioc-l1b-rf01
2	ACCL:L1B:0300	lcls-llrfcpu02	sioc-l1b-rf02

*CPU Node Name is referred to as <cpuname> in the commands shown below.

EPICS IOC Name is referred to as <iocname> in the commands shown below.

2. Chassis IPs

These are the IP addresses used in the LLRF internal network. They are the same for each cryomodule.

Rack	Chassis	IP
Cavities 1-4 (aka Rack A)	RES	192.168.0.100
Cavities 1-4 (aka Rack A)	RFS1 (cavities 1,2)	192.168.0.101
Cavities 1-4 (aka Rack A)	RFS2 (cavities 3,4)	192.168.0.102
Cavities 1-4 (aka Rack A)	PRC	192.168.0.103
Cavities 5-8 (aka Rack B)	RES	192.168.0.200
Cavities 5-8 (aka Rack B)	RFS1 (cavities 5,6)	192.168.0.201
Cavities 5-8 (aka Rack B)	RFS2 (cavities 7,8)	192.168.0.202
Cavities 5-8 (aka Rack B)	PRC	192.168.0.203

PRC=Precision Receiver Chassis

Reads cavity probe signals

RFS=RF Station

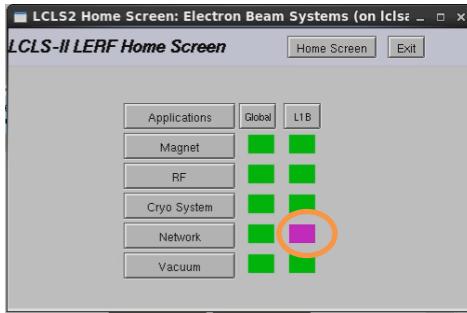
Provides RF drive; reads forward , reverse, detune signals

RES=Resonance/Interlock Chassis

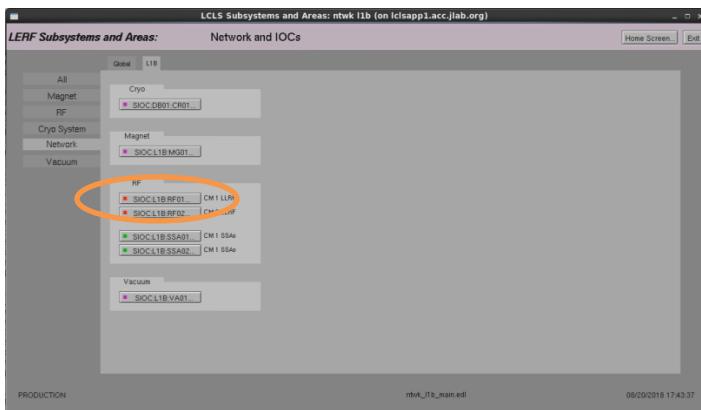
Controls tuners; performs interlock logic

3. Start/Restart the EPICS IOC

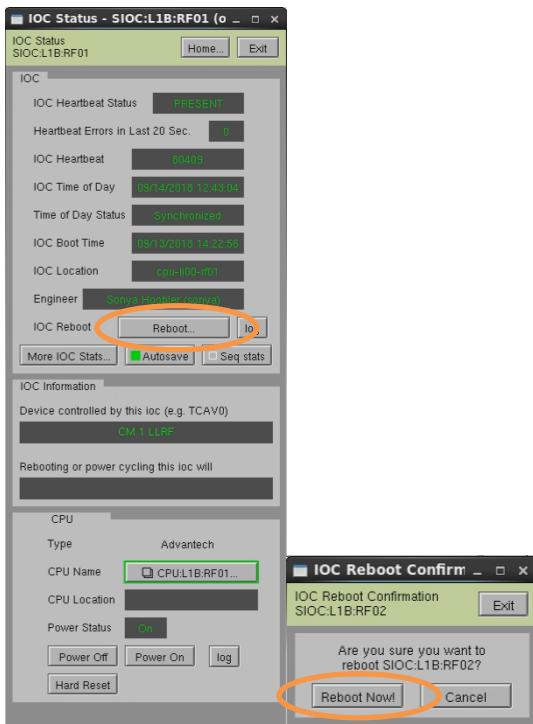
1. Log into LERF workstation or server
(lcls01/2/3/lclsapp1 with individual user id)
2. Type lerfhome&



3. Click on box intersecting Network and L1B



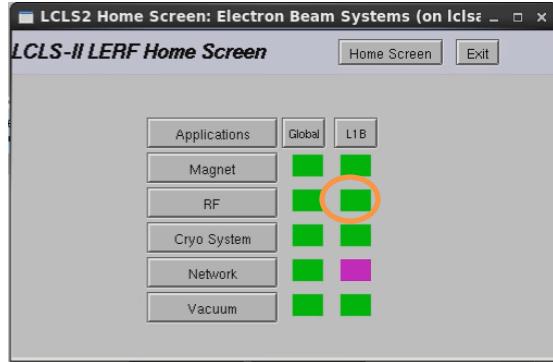
4. Click on IOC of interest



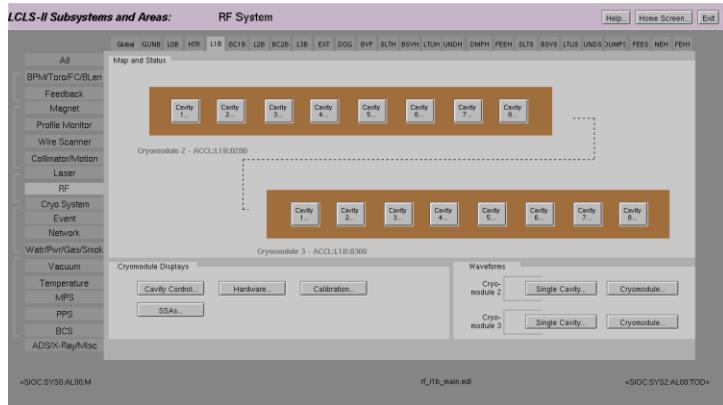
5. Click on 'Reboot'. Then click on 'Reboot Now!'

4. View RF EDM displays

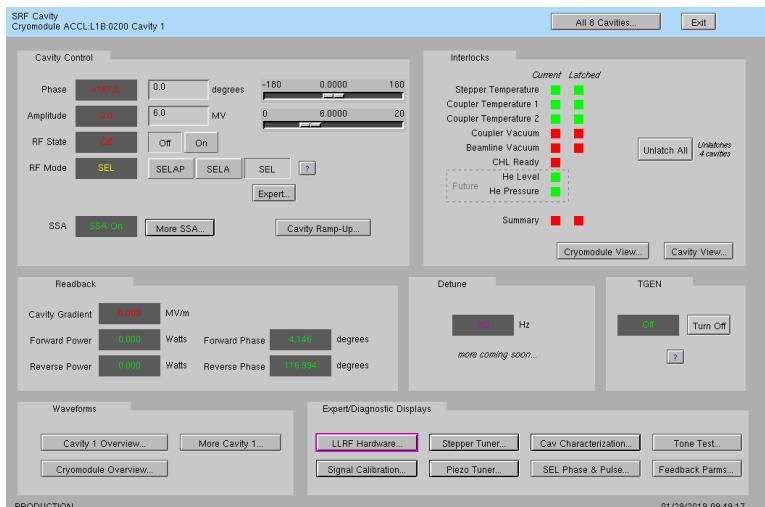
- Log into LERF workstation or server
(lcls01/2/3/lclsapp1 with individual user id)
- Type lrfhome&



- Click on box intersecting RF and L1B



L1B Overview Display

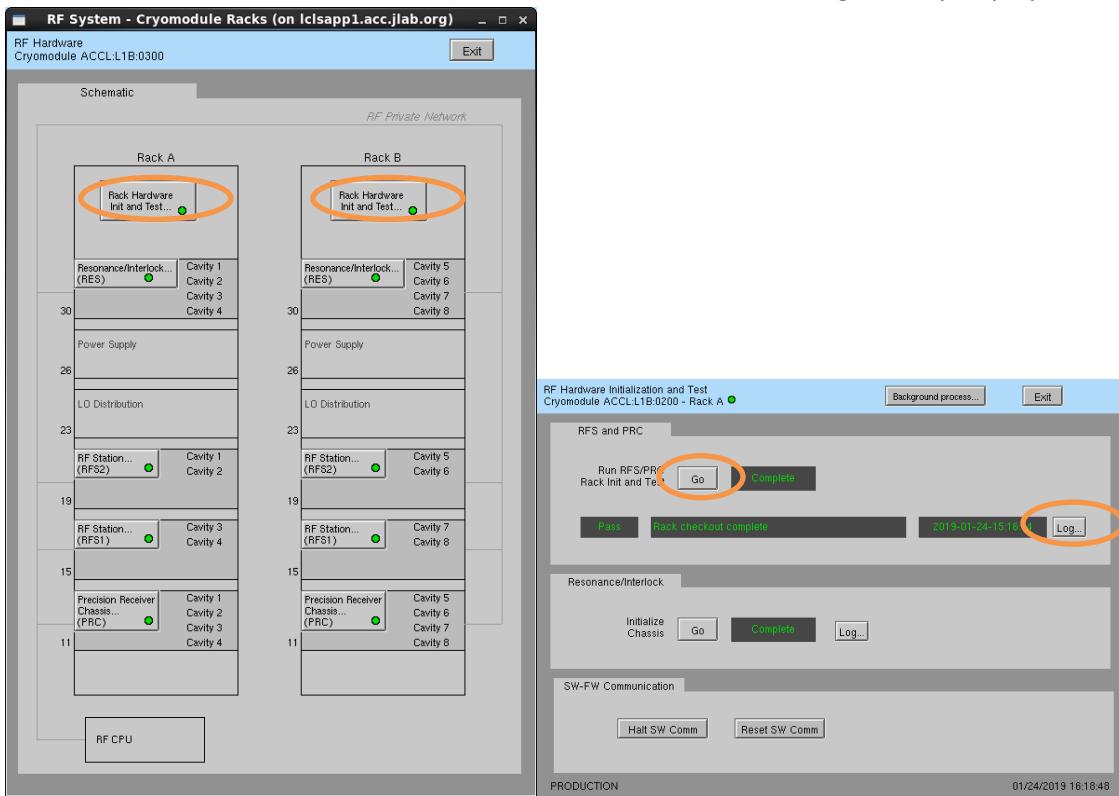


Single Cavity Display

5. Initialize/Checkout LLRF Hardware

Needed after power outage, hardware swap, etc.

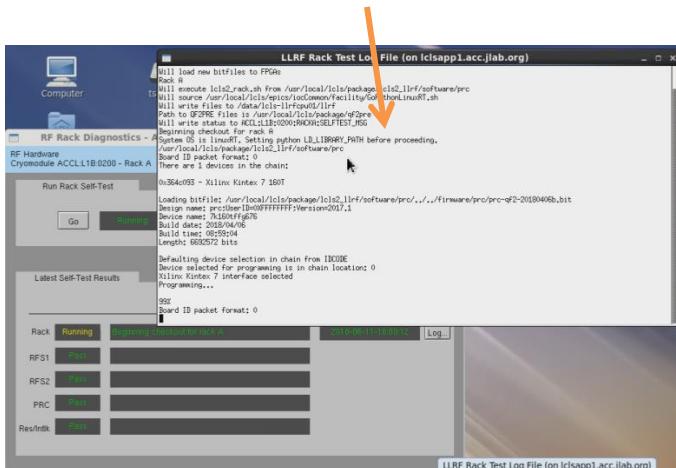
- Click on Hardware... (from either L1B Overview or Single Cavity display—see above).

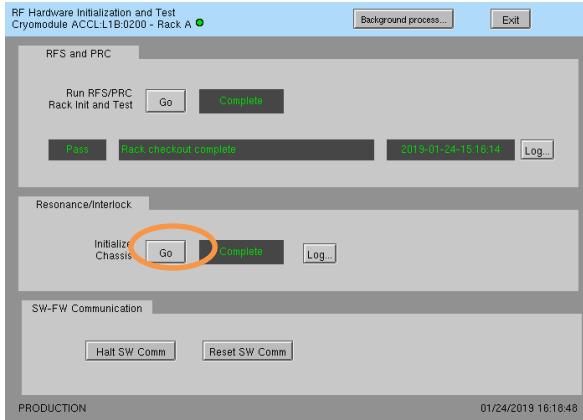


- For appropriate rack (A or B), open display labelled Rack Hardware Init and Test...
- Execute RFS/PRC initialization and checkout. Click Go
(The script will disable and then re-enable communication with EPICS.)

To view the rack test script output, click on Log...

It will open an xterm window and display the script output as it progresses.

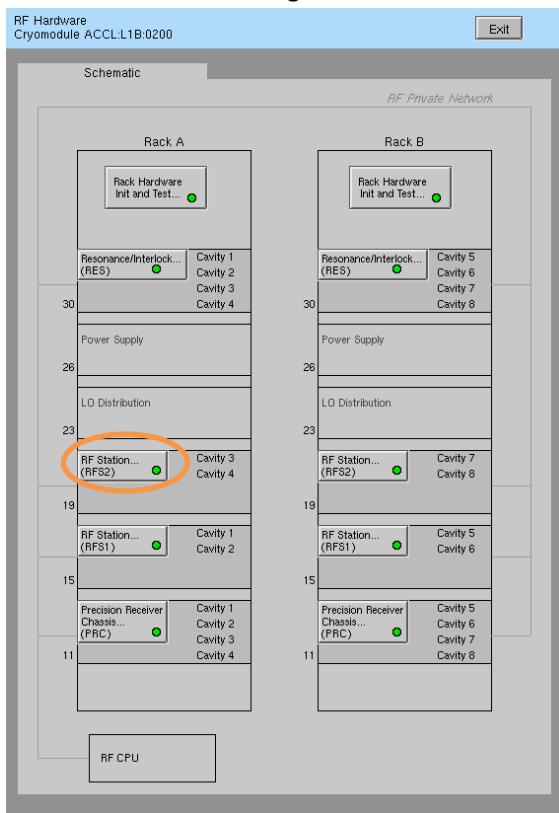


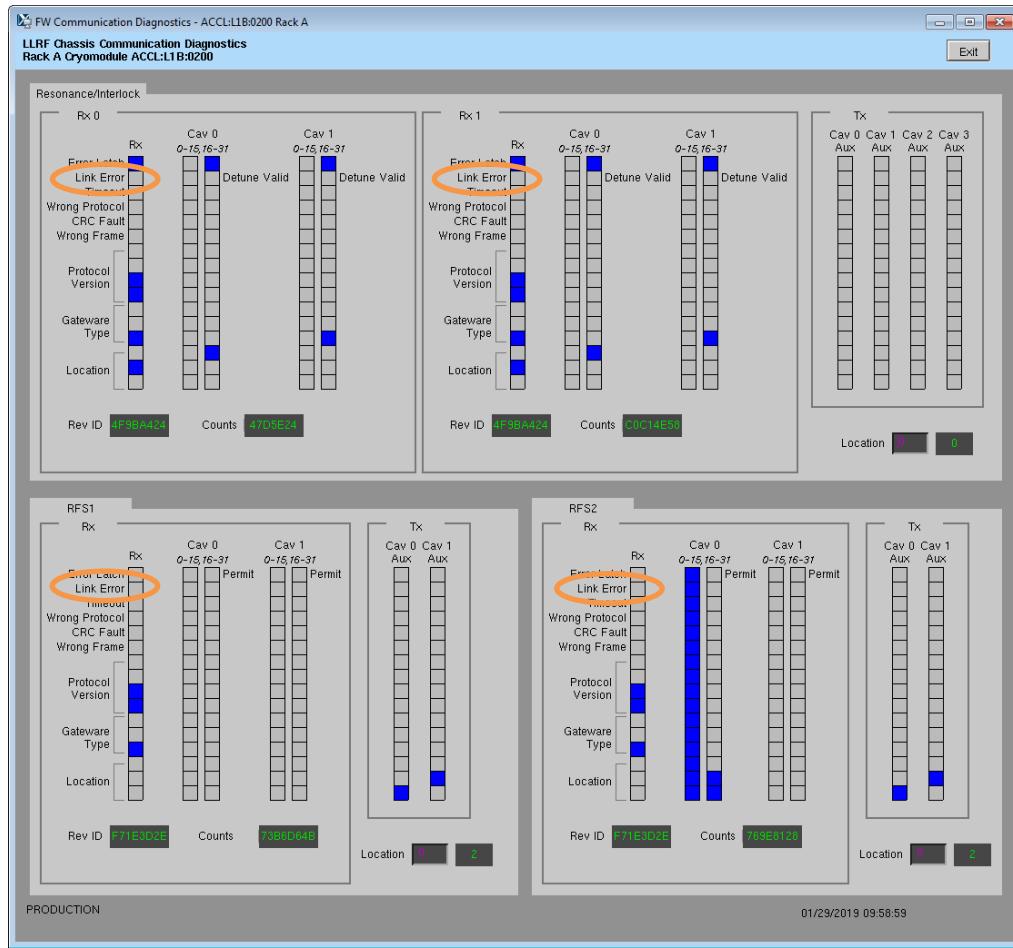
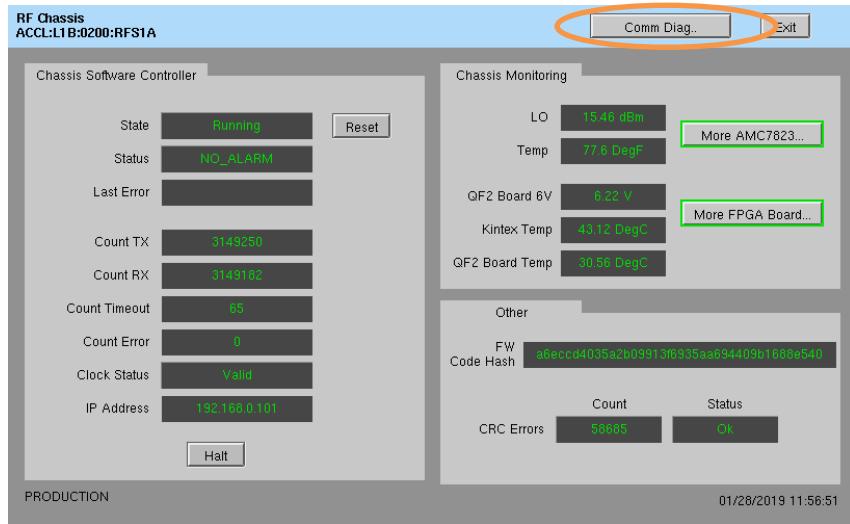


d. Initialize RES chassis. Click Go

(The script will disable and then re-enable communication with EPICS.)

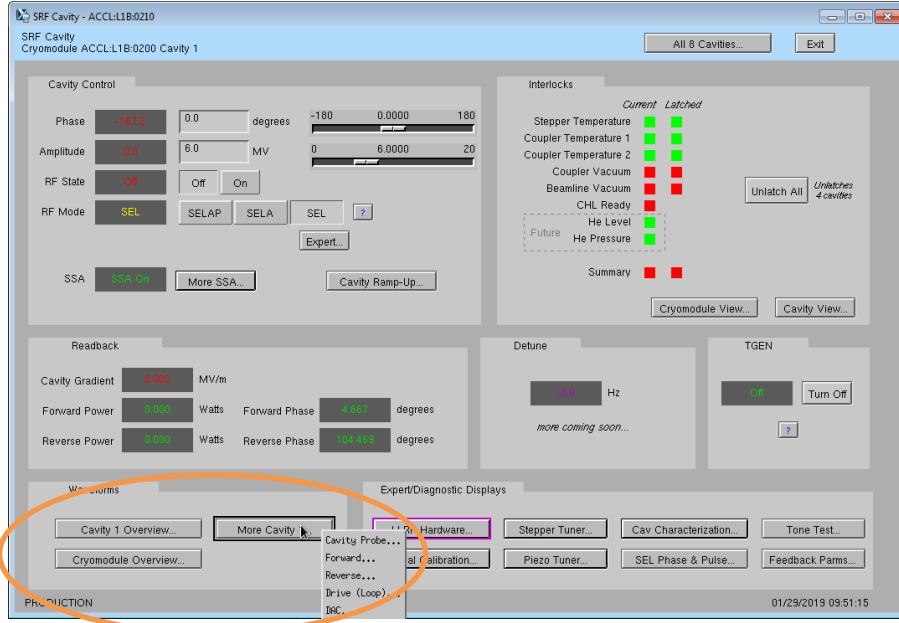
There are currently no automated checks for RFS<->RES communication. You'll need to look at the diagnostic display. From the Hardware display, for the appropriate rack, click on an individual chassis, then click on Comm Diag...





The top row shows the RES status; the bottom the RFS status. In the 'Rx' sections, the 'Link Error' bit is set (blue) if there is a problem. (This snapshot shows no communication errors.) There is other useful data on this display too.

6. View RF Waveform Plots

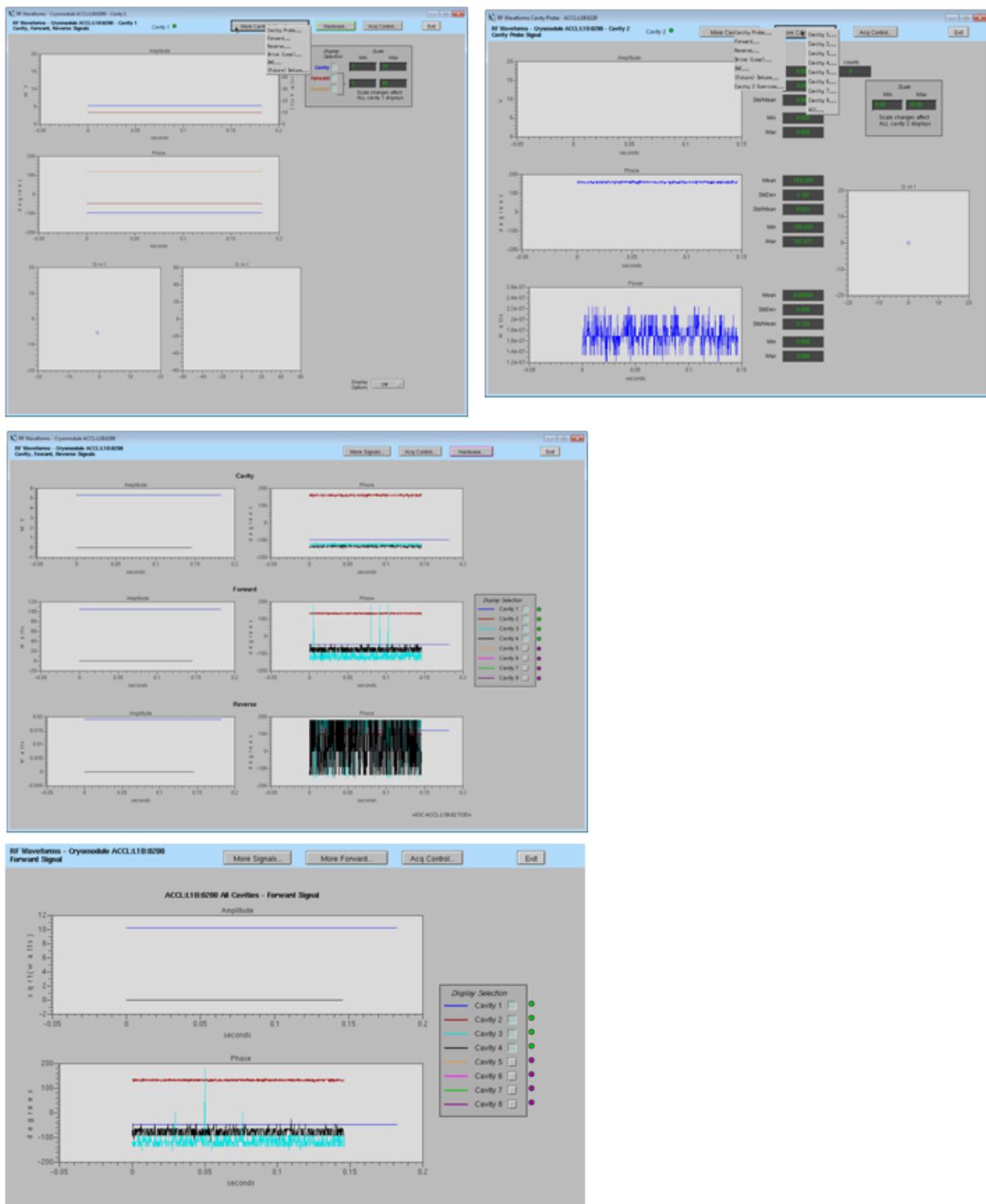


Cavity Overview -- Cavity, Forward, Reverse signals for single cavity

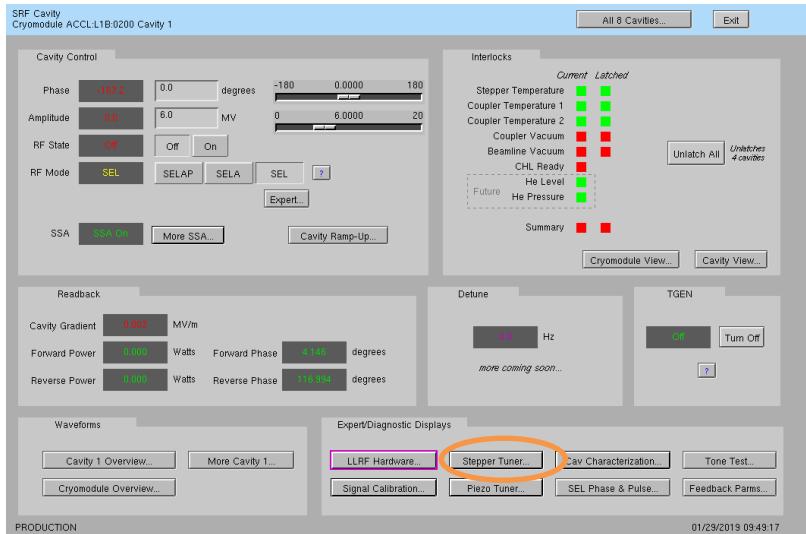
Cryomodule Overview -- Cavity, Forward, Reverse signals for all 8 cavities

More Cavity -- Drop-down menu for single-cavity single-signal display

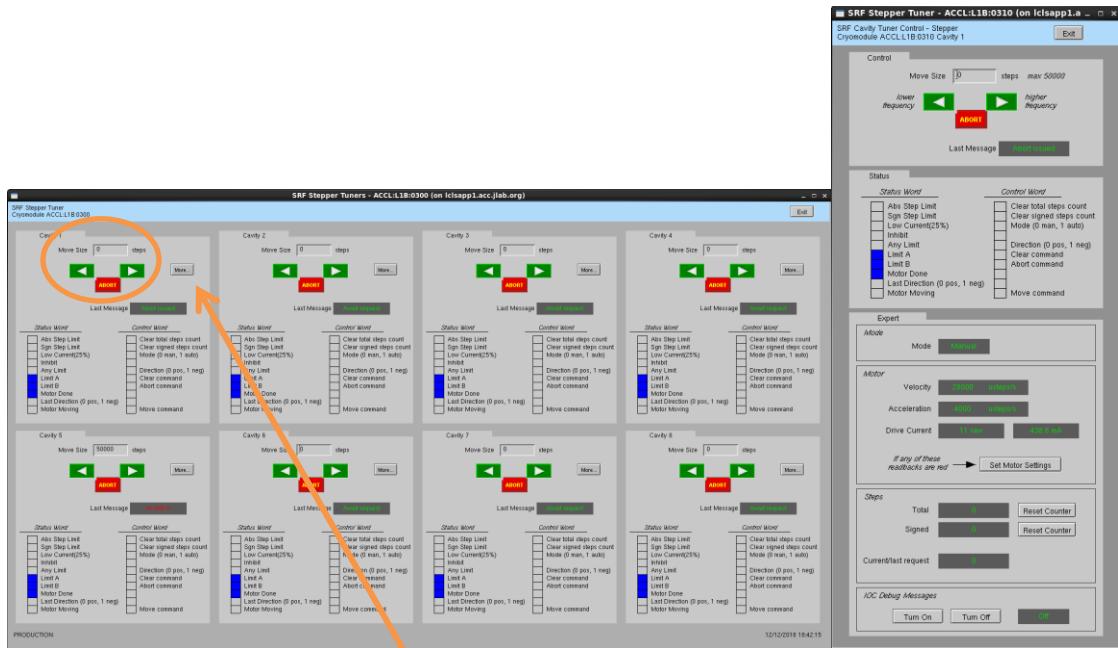
From those displays, you can navigate to other cavities, other signals, or display a single signal for all 8 cavities. Example displayshots:



7. Control Stepper Motor Tuner

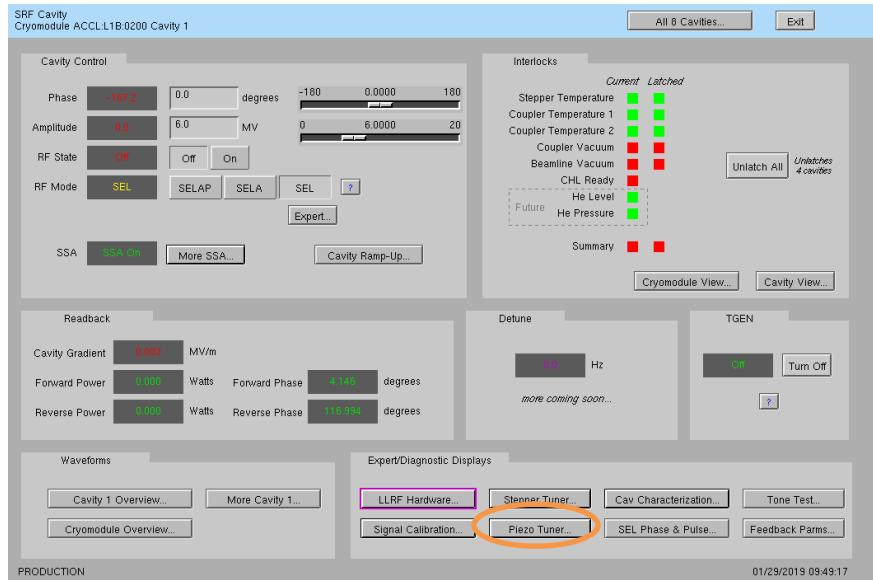


a. Click on Stepper Tuner...

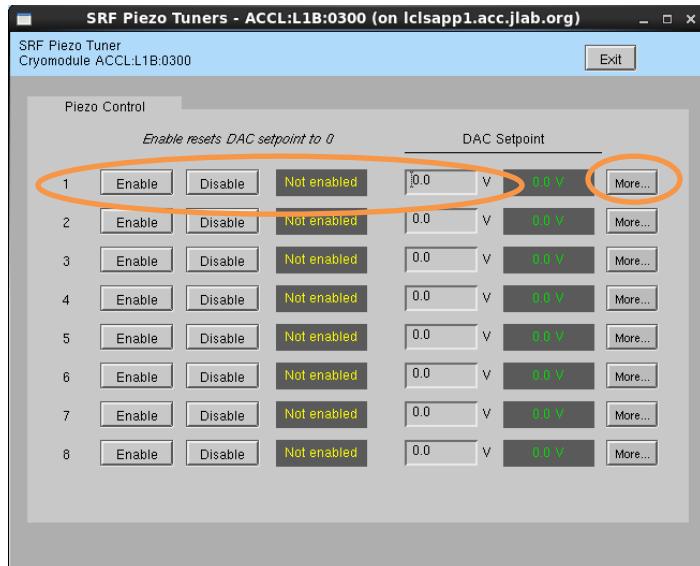


b. 8-cavity display. From here you can enter number of steps and then click the green arrow to go in one direction or the other. (Relationship between direction and frequency is not yet known.) Click More... for single tuner display

8. Control Piezo Tuner

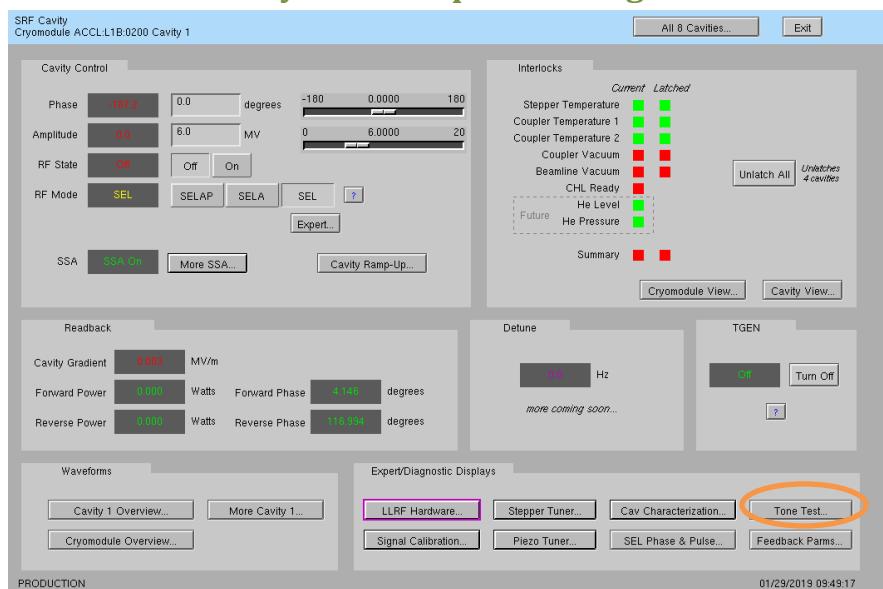


a. Click on Piezo Tuner...



b. 8-cavity display. From here you can enable the piezo tuner and enter a DC DAC voltage. Click More... for single piezo tuner display

9. Drive Cavity With Simple Tone Signal



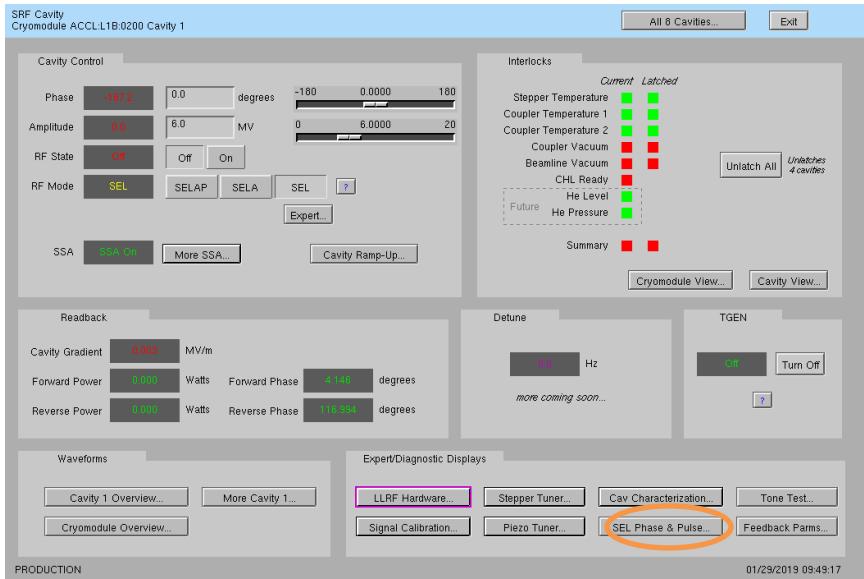
a. Click on Tone Test...



b. Turn desired cavity 'On', then enter number of DAC counts...

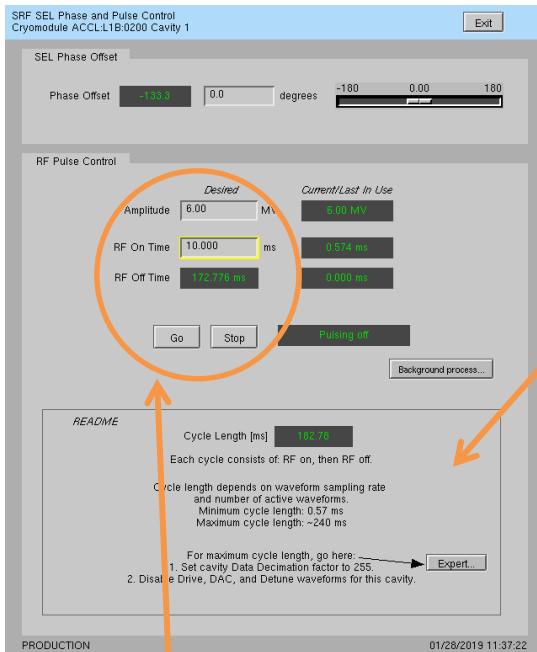
c. When done, click 'Off'

10. Run RF In SEL Pulsed Mode



a. Click on SEL Phase & Pulse...

The settings/options for pulsed mode are more complicated than you'd think. It's a good idea to read the README at the bottom of the display.



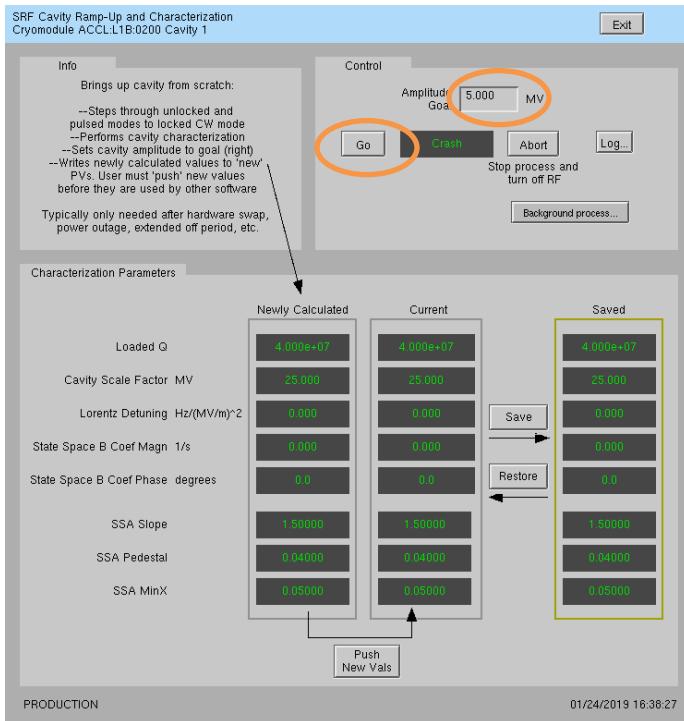
- b. Enter desired amplitude (settings will be 'yellow' if they do not match Current/Last In Use)
- c. Enter desired RF pulse length
- d. Click Go
- e. When done, click Stop
- f. To update amplitude or time settings, enter new values and then click Go again

11. Ramp Cavity to CW/SELAP and Perform Cavity Characterization

This is used to bring up a cavity ‘from scratch’ or if there is some need to re-characterize the cavity/SSA. You do not need to run this every time you turn a cavity on.



a. Click on Cavity Ramp-Up...

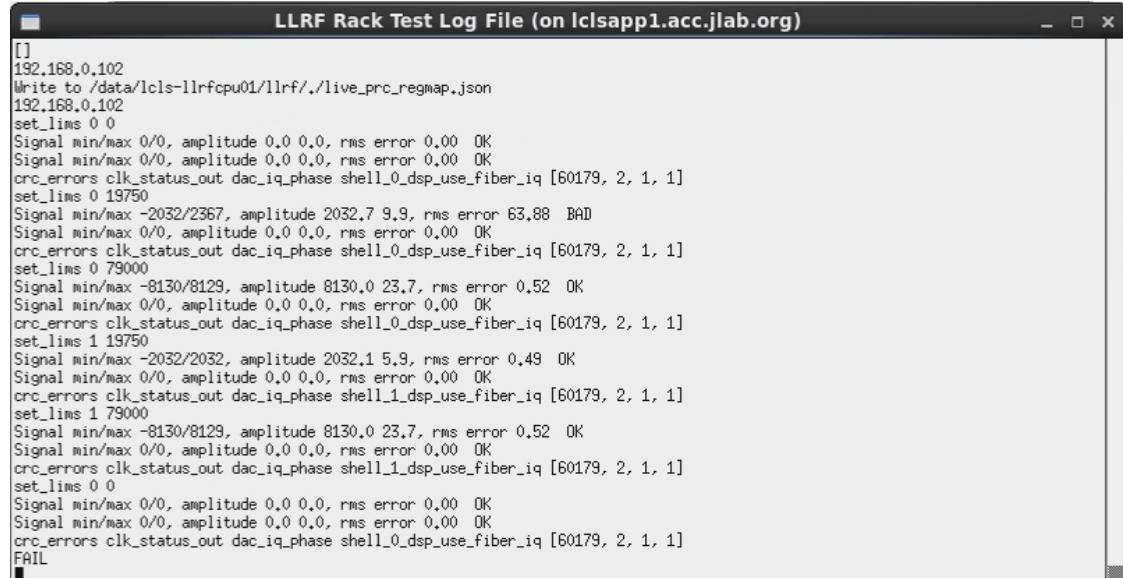


- Set Amplitude Goal and click Go
- Future (not yet done 1/29/2019): the script will write calculated characterization parameters to the Newly Calculated values on the display. You can review these and if they seem reasonable, click Push New Vals. You can also save the Current values as a known good set—to possibly restore in future.

II. Occasional Issues

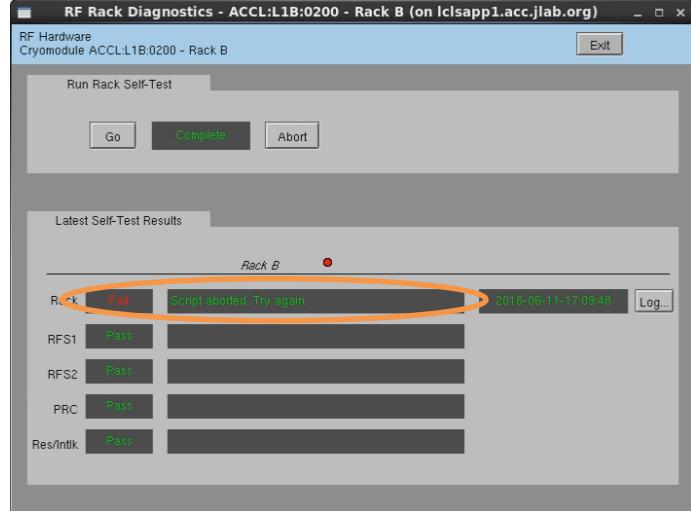
1. Mystery Rack Checkout Error

We occasionally see a problem during the first cryomodule rack A checkout. This is what it looks like:



The screenshot shows a terminal window titled "LLRF Rack Test Log File (on lclsapp1.acc.jlab.org)". The log file contains several error messages related to signal amplitude and RMS errors, followed by a "FAIL" message at the end.

```
[]
192.168.0.102
Write to /data/lcls-l1rfcpu01/l1rf ./live_prc_regmap.json
192.168.0.102
set_lims 0 0
Signal min/max 0/0, amplitude 0.0 0.0, rms error 0.00 OK
Signal min/max 0/0, amplitude 0.0 0.0, rms error 0.00 OK
crc_errors clk_status_out dac_iq_phase shell1_0_dsp_use_fiber_iq [60179, 2, 1, 1]
set_lims 0 19750
Signal min/max -2032/2367, amplitude 2032.7 9.9, rms error 63.88 BAD
Signal min/max 0/0, amplitude 0.0 0.0, rms error 0.00 OK
crc_errors clk_status_out dac_iq_phase shell1_0_dsp_use_fiber_iq [60179, 2, 1, 1]
set_lims 0 79000
Signal min/max -8130/8129, amplitude 8130.0 23.7, rms error 0.52 OK
Signal min/max 0/0, amplitude 0.0 0.0, rms error 0.00 OK
crc_errors clk_status_out dac_iq_phase shell1_0_dsp_use_fiber_iq [60179, 2, 1, 1]
set_lims 1 19750
Signal min/max -2032/2032, amplitude 2032.1 5.9, rms error 0.49 OK
Signal min/max 0/0, amplitude 0.0 0.0, rms error 0.00 OK
crc_errors clk_status_out dac_iq_phase shell1_1_dsp_use_fiber_iq [60179, 2, 1, 1]
set_lims 1 79000
Signal min/max -8130/8129, amplitude 8130.0 23.7, rms error 0.52 OK
Signal min/max 0/0, amplitude 0.0 0.0, rms error 0.00 OK
crc_errors clk_status_out dac_iq_phase shell1_1_dsp_use_fiber_iq [60179, 2, 1, 1]
set_lims 0 0
Signal min/max 0/0, amplitude 0.0 0.0, rms error 0.00 OK
Signal min/max 0/0, amplitude 0.0 0.0, rms error 0.00 OK
crc_errors clk_status_out dac_iq_phase shell1_0_dsp_use_fiber_iq [60179, 2, 1, 1]
FAIL
```

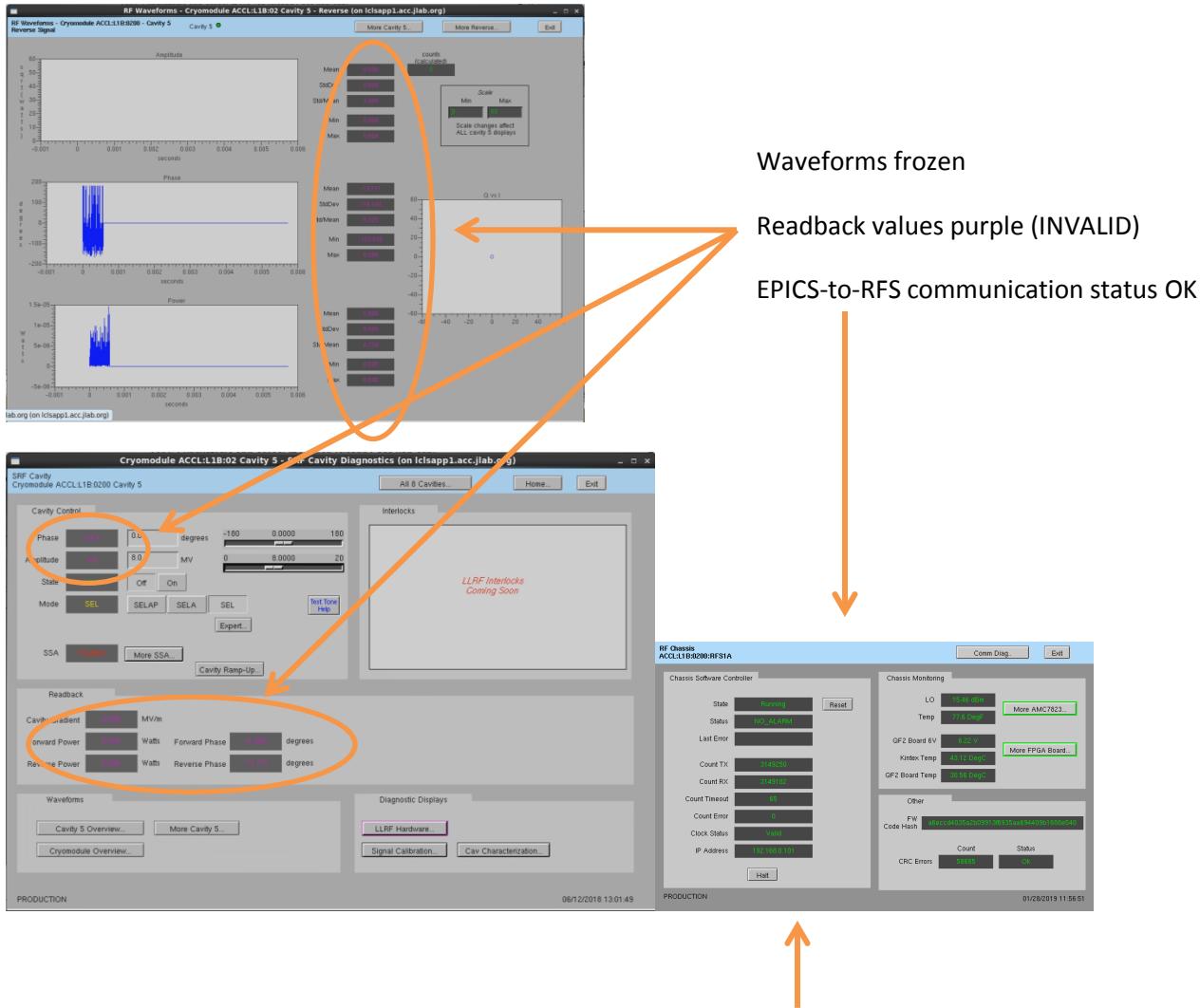
The screenshot shows a software interface titled "RF Rack Diagnostics - ACCL:L1B:0200 - Rack B (on lclsapp1.acc.jlab.org)". It displays a "Run Rack Self-Test" button and a "Latest Self-Test Results" table. The table lists hardware components and their status: RACK (Fail, Script aborted. Try again), RFS1 (Pass), RFS2 (Pass), PRC (Pass), and Res/Intlk (Pass). An orange circle highlights the "Fail" status for the RACK component.

Latest Self-Test Results	
Rack B	
RACK	Fail Script aborted. Try again.
RFS1	Pass
RFS2	Pass
PRC	Pass
Res/Intlk	Pass

It is an intermittent failure and is not understood. You'll have to simply re-run rack checkout and hope it passes. (We have observed that if the system was previously set up correctly and there has been no outage/hardware swap since, it will probably work fine in spite of this error.)

2. Recover Non-Updating EPICS Waveforms

Once in a while, I've seen the EPICS waveform data stop updating. Displays will look like this:



3. Ping Test

To test if a chassis pings from a CPU:

- a. Log into LERF workstation or server
(lcls01/2/3/lclsapp1 with individual user id)
- b. Log into cpu (using CPU Node Name from Section 1):

iocConsole <cpuname>

OR

ssh laci@<cpuname>

(If prompted for login, type ‘laci’ and hit enter.)

- c. Type: ping <ip>

4. View the EPICS IOC Console

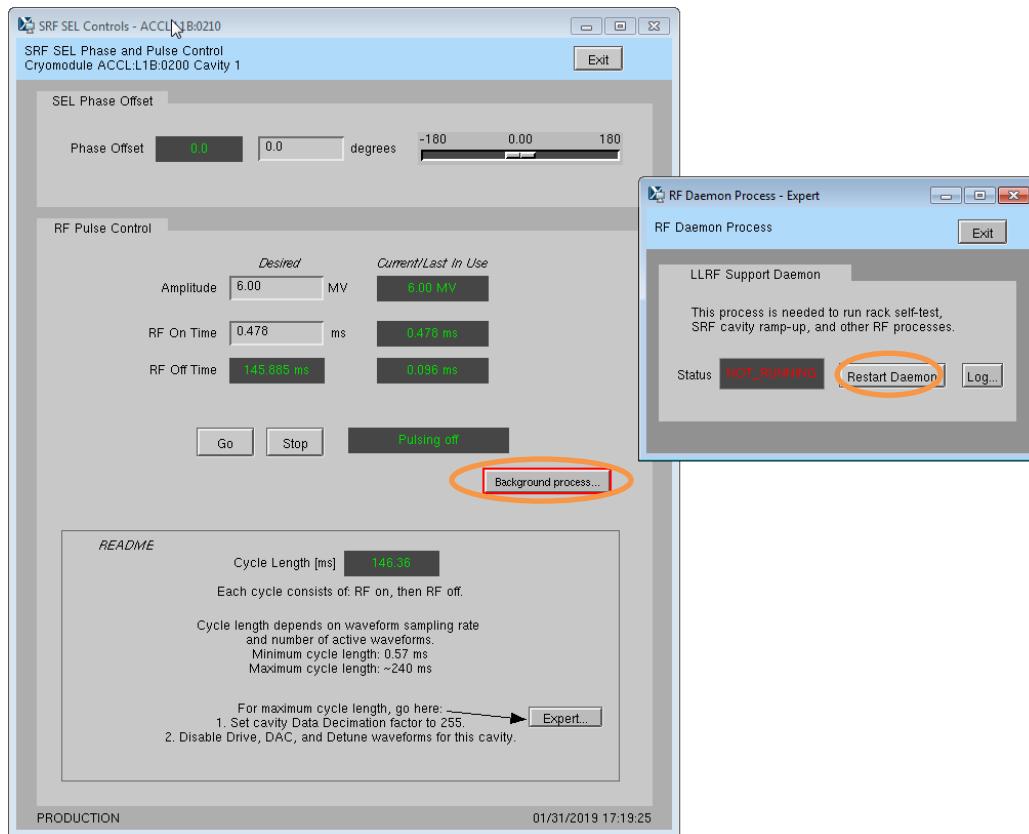
- a. Log into LERF workstation or server
(lcls01/2/3/lclsapp1 with individual user id)
- b. iocConsole <iocname>
- c. To exit viewer, press these 3 keys together: [Ctrl](#), [a](#), [d](#).
- d. To scroll up in viewer, press these 3 keys together: [Ctrl](#), [a](#), [. Then use arrows to move up/down. To exit scroll mode, press these 3 keys together: [Ctrl](#), [a](#),].

(If you inadvertently kill the ioc, you can restart it using the instructions in section 2.)

5. RF ‘Background Process’

Much of the current RF functionality (rack checkout, pulse control, cavity ramp, etc.) is performed by scripts external to the EPICS IOC. This is facilitated by a continuously running background process that is driven by EPICS PVs. This allows us to execute these functions from EPICS PVs on EDM displays—and not have to manually run scripts from the command line.

On every display that relies on this, there is a button titled ‘Background process...’. If that process is not running, there will be a red rectangle around it. Click on that button to open a display from which you can start/restart the process. Example:



This process will not successfully launch if the EPICS PVs it relies on are not all online. Both LLRF EPICS IOCs must be on.