

# *Update on PRad GEMs and Electronics*

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# Status of PRad GEM2 assembly

- Construction in Clean Room of PRad GEM2 is completed, out in the Detector Lab
- Individual HV sectors test ongoing, (half 96 sectors tested OK) the other half to be tested today
- Chamber will be sealed against gas week over the weekend
- Mounting HV resistive divider and spark protection resistors board (Monday 14 and Tuesday 15)
- Final HV test to validate the construction (Wednesday 16)
- Should be ready for response uniformity test on X-ray box (Thursday 17)



# Status of SRS-SRU Electronics

- Ben is testing the 10 Gb firmware upgrade compatible with APV electronics.
  - Preliminary tests **OK** ⇒ data transmission with the new firmware is working
  - Further tests and debugging of the firmware is ongoing with Ben
  - **SRU Hardware**: One SRU **OK**, the other one need a fix, we have an idea of what the problem might be after some e-mail exchange with SRS developers from CERN
- **Next step**: test the 10 Gb firmware with the full chain (APV25-ADC-FEC-SRU Krishna & myself)
  - Look at APV25 signal with DATE software and compare 1Gb and 10 Gb link data (**after Ben debugging work is completed**)
- Rate performance tests is the next step with JLab PCIe-TI & CODA
  - This is the next part of the development (Brian)
  - Need to meet again with JLab DAQ next week (**Would suggest Tuesday 15<sup>th</sup>**)