The PRad-II Flash ADC-based Upgraded DAQ.



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PRad used a FASTBUS-based DAQ



A FASTBUS-based DAQ with custom summing modules and TDCs DAQ rate ~ 4 kHz & 80-90% live-time during production.

Image courtesy of Chao Peng

PRad used custom summing modules

Dynode signals from all PMTs summed by 52 UVA120A custom linear summing modules 1 copy of output digitized using 52 CAEN v1990 TDC modules 2nd copy summed using NIM Fan-In modules to produce the Total Sum Trigger



DAQ rate ~ 4 kHz

Image courtesy of Chao Peng

PRad-II will upgrade to a FADC-based DAQ



Modern flash ADC-based pipelined DAQ with robust trigger efficiency monitoring

Image courtesy of Chao Peng

There are several key advantages to the **FADC-based DAQ**

Advantages of a modern flash ADC-based pipelined DAQ



Eliminates the need for 100s of meters of delay cables

Better gain and pedestal stability and monitoring





Improved rejection of accidentals

Essentially dead time free

Improved trigger efficiency with continuous monitoring

Provides timing information for each crystal with better than 1.5 ns resolution, needed for the veto scintillators enabling access to the lowest Q² in e-p scattering experiments.

7 fold improvement in DAQ rate

Images courtesy of Weizhi Xiong

5 / 7





The upgraded DAQ enables 7-fold improved rate

Critical for the factor of ~4 reduction in the statistical uncertainty of PRad-II



DAQ Hardware related resources were requested in the NSF pre-proposal

Qty	Description	cost		Notes
1	VXS crate	<mark>\$16,000</mark>	<mark>\$16,000</mark>	Includes high current supply and high RPM fans
16	FADC250	\$6,000	\$96,000	16 boards per crate (256 channel total) Lemo inputs
1	SD	<mark>\$2,000</mark>	<mark>\$2,000</mark>	Signal distribution board
1	VTP	\$9,000	\$9,000	Trigger processor
1	ТІ	<mark>\$1,500</mark>	<mark>\$1,500</mark>	Trigger interface
1	CPU	\$5,500	\$5,500	Linux readout controller
		Total:	<mark>\$130,000</mark>	

14 crates + 1 spare : \$130,000 x 15 = \$1.95 Million