

HKS DAQ meeting

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GRADUATE
SCHOOL OF
FACULTY OF **SCIENCE**
KYOTO UNIVERSITY

Experimental readiness review (Nov 2024)

What to prepare for ERR?

- Circuit map
- Necessary electronics / cables
- Cable length / path (using HMS crates for modules?)
- Trigger logic
 - FPGA logic / test
- Procedure how to adjust the e^-K^+ coincidence
- Trigger conditions to be prepared

Detector test / FPGA test

Mon	Tue	Wed	Thu	Fri	Sat	Sun
		13 Arrival	14	15 RWI test (Teppe)	16	17
18	19	20	21	22 HKS meeting	23	24
25	26	27 Leaving				

MRPC

- Possible cost, schedule and manpower
- Engineering
 - Installation
 - Frame (in particular, z size = thickness)
- Electronics / cables etc.

Backup

Experimental setup at Hall C



