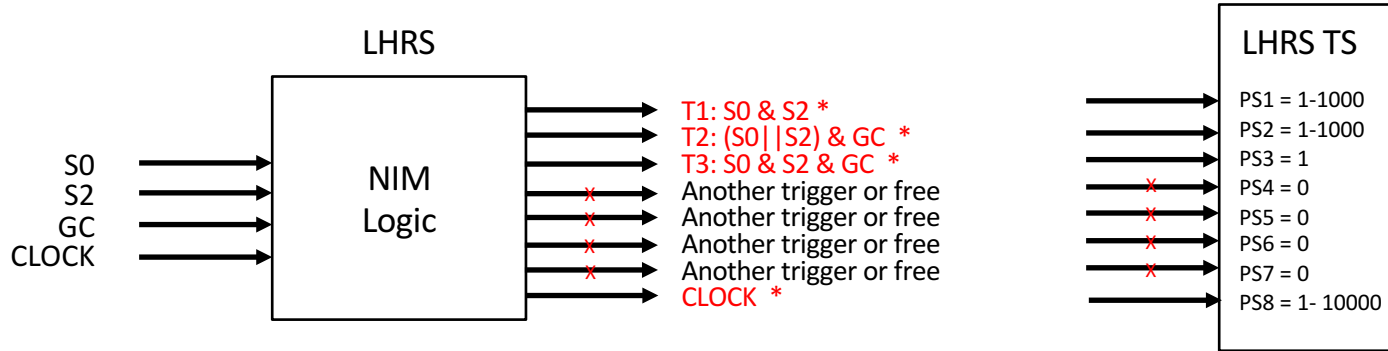
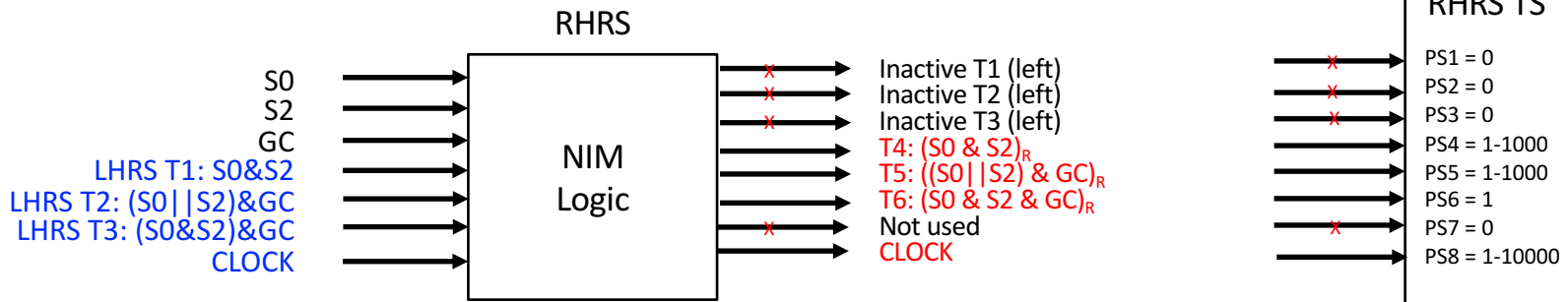


# Trigger Logic for 2 DAQ mode



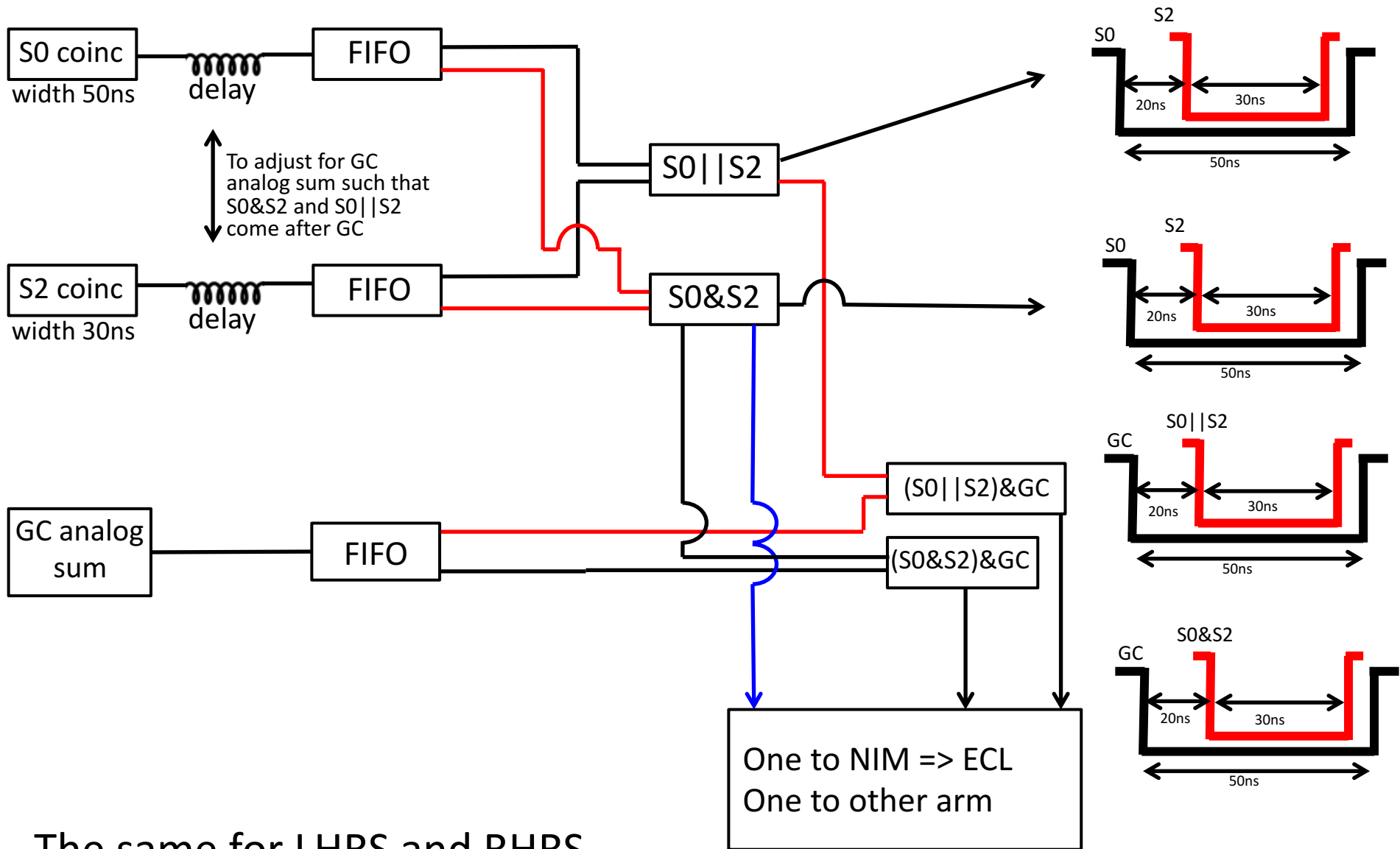
X : in/output not used in trigger design

\* Connections to RHRS are intact but triggers are disabled on RHRS by TS prescales

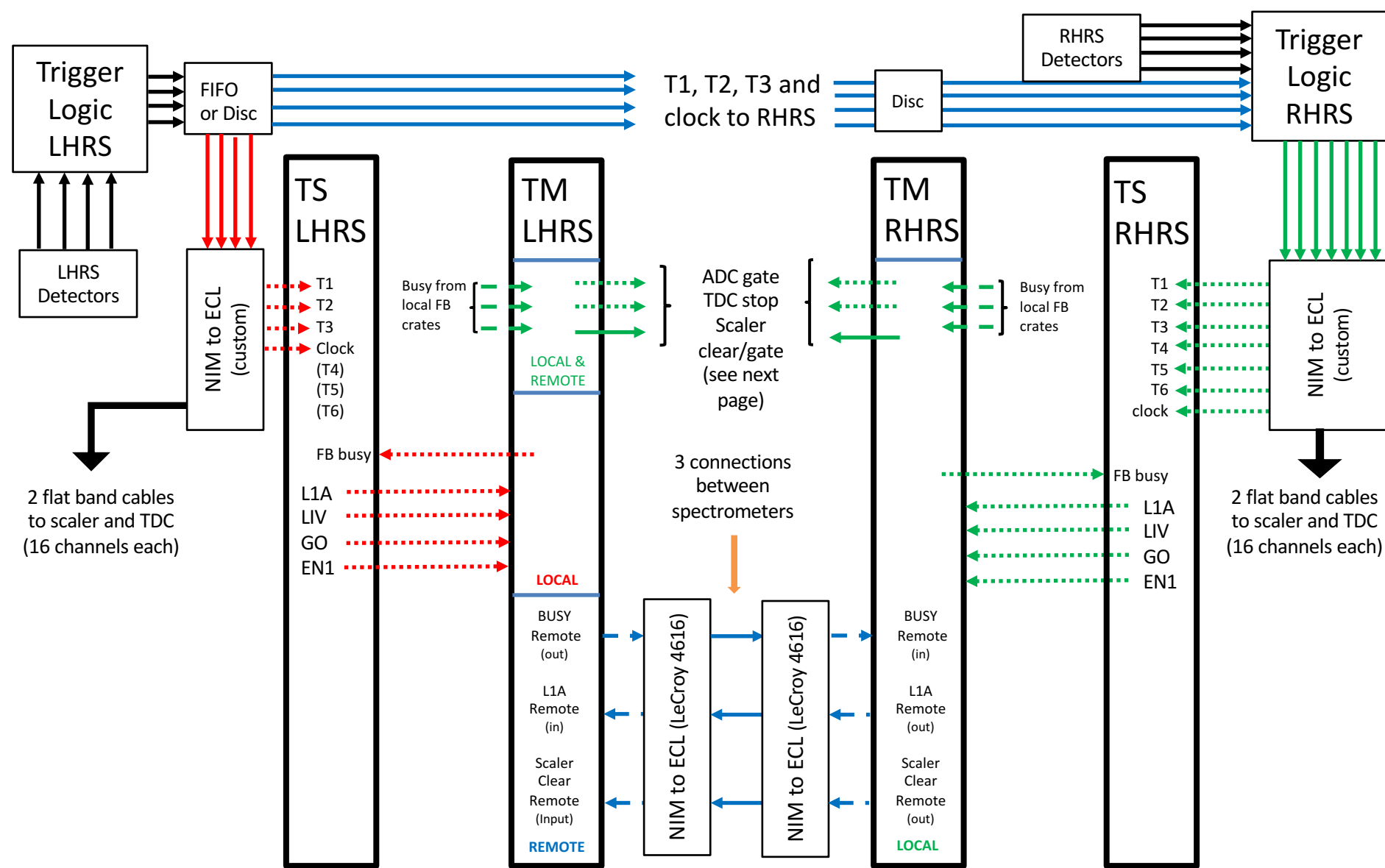


- Some of the signals on the RHRS have to be delayed to be in time with LHRs triggers
- T1 – T3 LHRs triggers - similar for all experiments
- T4 – T6 RHRS triggers (single or coincidence triggers)

# Single Arm Triggers ( $S0\&S2$ ); $(S0\&S2)\&GC$ ; $(S0||S2)\&GC$



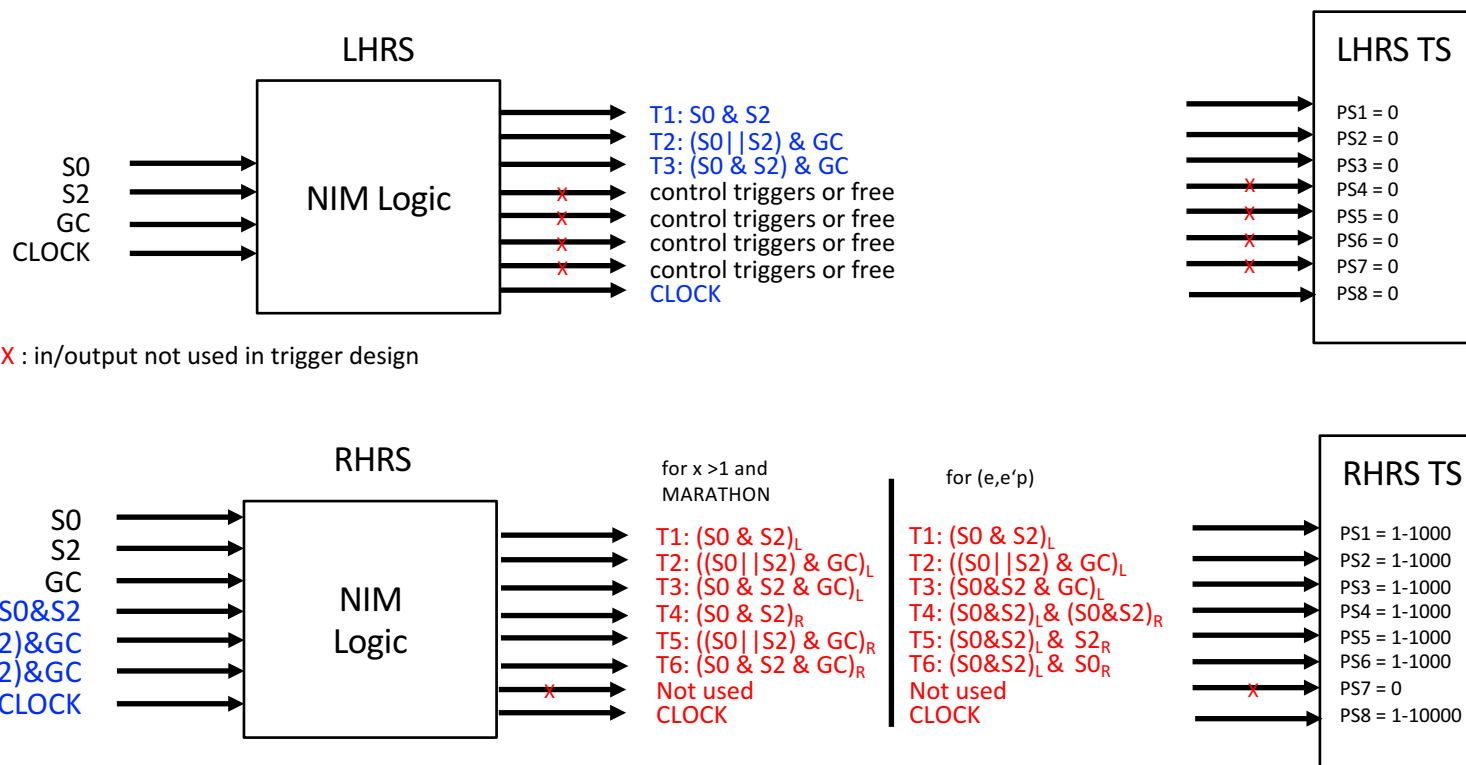
The same for LHRS and RHRS



- NIM connection
- ..... ECL connection on twisted pair
- ECL on LEMO TWINAX connection
- · - · - · ECL on TWINNAX to 2pin

- for 2 DAQ setup (MARATHON)
- for 1 DAQ setup
- for both setups

# Trigger Logic for 1 DAQ mode



- Some of the signals on the RHRs have to be delayed to be in time with LHRs triggers
- T1 – T3 LHRs triggers - similar for all experiments
- T4 – T6 RHRs triggers (single or coincidence triggers)

# Coincidence triggers

